

Product Specifications

15.0" XGA Color TFT-LCD Module Model Name:M150XN04 V.1

(♦) Preliminary Specifications() Final Specifications



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Version and Date	Page	Old description	New Description	Remark
0.1 2002/01/30	All	First Edition for Customer	All	
0.2 2002/05/27	12	Connecter Pin Configuration	Pin 2 HV,Lamp High Voltage	
0.2 2002/5/27	17	Lamp connecter	Pin 2 Lamp High Voltage	
0.2 2002/5/27	17	725 rms,PCFL 5.5mA, 21Watt	725rms,PCFL 5.5mA, 16 Watt	
0.3 2002/08/22	5	View Angle : 75/75/70/70 CR=5	View Angle: 70/70/60/60 CR=10,	
	11		80/80/70/80 CR=5	
0.3 2002/08/22	11		Add color chromaticity	
0.4 2002/10/03	18	LCD Drive Current (IDD) [mA]:	LCD Drive Current (IDD) [mA]:	Change
		600 (typ.); 700 (Max)	1000 (typ.); 1150 (Max)	
	18	LCD Drive Power Consumption	LCD Drive Power Consumption	
		2.0 (typ.); 2.4 (Max)	3.3 (typ.); 3.8 (Max)	

1.0 Handling Precautions

- 1) Since front polarizer is easily damaged, pay attention not to scratch it.
- 2) Be sure to turn off power supply when inserting or disconnecting from input connector.
- 3) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
- 4) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
- 5) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface.
- 6) Since CMOS LSI is used in this module, take care of static electricity and insure human earth when handling.
- 7) Do not open nor modify the Module Assembly.
- 8) Do not press the reflector sheet at the back of the module to any directions.
- 9) In case if a Module has to be put back into the packing container slot after once it was taken out from the container, do not press the center of the CCFL Reflector edge. Instead, press at the far ends of the CFL Reflector edge softly. Otherwise the TFT Module may be damaged.
- 10) At the insertion or removal of the Signal Interface Connector, be sure not to rotate nor tilt the Interface Connector of the TFT Module.
- 11) After installation of the TFT Module into an enclosure (LCD monitor housing, for example), do not twist nor bend the TFT Module even momentary. At designing the enclosure, it should be taken into consideration that no bending/twisting forces are applied to the TFT Module from outside. Otherwise the TFT Module may be damaged.
- 12) Cold cathode fluorescent lamp in LCD contains a small amount of mercury. Please follow local ordinances or regulations for disposal.
- 13) Small amount of materials having no flammability grade is used in the LCD module. The LCD module should be supplied by power complied with requirements of Limited Power Source(2.11, IEC60950 or UL1950), or be applied exemption.
- 14) The LCD module is designed so that the CFL in it is supplied by Limited Current Circuit(2.4, IEC60950 or UL1950). Do not connect the CFL in Hazardous Voltage Circuit.

2.0 General Description

This specification applies to the 15.0 inch Color TFT-LCD Module M150XN05. The display supports the XGA (1024(H) x 768(V)) screen format and 262,144 colors (RGB 6-bits data). All input signals are 2 Channel TTL interface compatible. This module does not contain an inverter card for backlight.

Features

-XGA 1024(H) x 768(V) resolution

-4 CCFLs (Cold cathode Fluorescent Lamp)

-High contrast ratio, high aperture ratio

-Wide viewing angle

-High speed response

-Low power consumption

Application

Desktop monitors

2.1 Display Characteristics

The following items are characteristics summary on the table under 25 • condition:

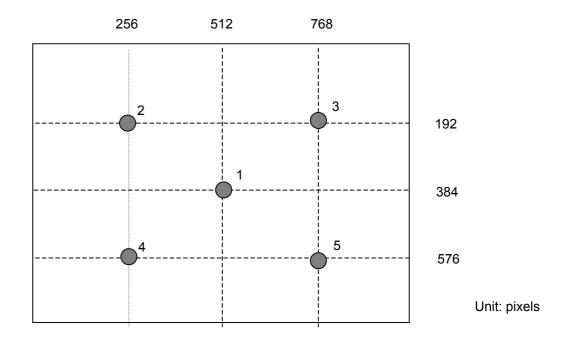
ITEMS	Unit	SPECIFICATIONS		
Screen diagonal	[mm]	381 (15")		
Outline dimension [mm]		326 x 249.0 x 14.68(max)		
Display Area	[mm]	304.128 (H) x 228.096(38.1cm diagonal)		
Resolution		1024(R,G,B x 3) x 768		
Pixel Pitch	[mm]	0.297 x 0.297		
Pixel Arrangement		R.G.B. Vertical Stripe		
Display Mode		TN mode, Normally White		
Typical white Luminance.	[cd/m ²]	500nit (typ) @5.5mA (note 1)		
Brightness uniformity		80% typ. (note 2)		
Luminance uniformity		1.7 max.(Note 3)		
Crosstalk (at 60Hz)		1.2% max. (note 4)		
Contrast Ratio		400 : 1 typ.		
Support Colors		262,144 colors (6-bit for R,G,B)		
Chromaticity(CIE1931)		0.313		
White-x				
		0.329		
White-y				
Color Gamut		60% typ., of NTSC coverage		
Viewing angle		70(left),70(right),60(up),60(down) CR=10		
		80(left), 80(right),70(up),80(down) CR=5		
Response Time	[msec]	16ms typ. (Tr +Tf)		
Nominal Input Voltage VDD	[Volt]	+3.3 V		
Power Consumption	[Watt]	17(typ)@5.5mA (All Black Pattern)		
(VDD line + CCFL line)				
Electrical Interface		TTL 2 port		
Frame rate	[Hz]	60Hz typ., 75Hz max.		
Weight	[Grams]	1350 typ.		

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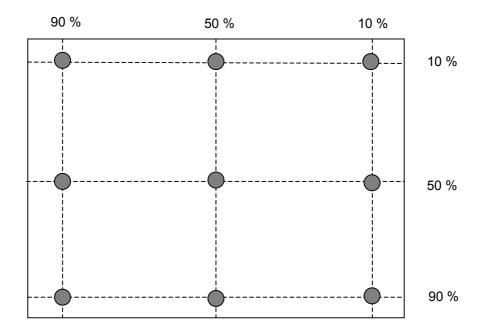
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Mounting method		Side mounting	
Surface treatment		Anti-glare, hard coating (3H)	
Temperature Range			
Operating	[°C]	0 to +50	
Storage (Shipping)	[°C]	-20 to +60	



Note 1: Brightness is measured at the center point of brightness value with all pixels displaying white.



Unit: percentage of dimension of display area

Note 2: Brightness uniformity of these 9 points is defined as below:

(Min. brightness / Max. brightness) x 100%

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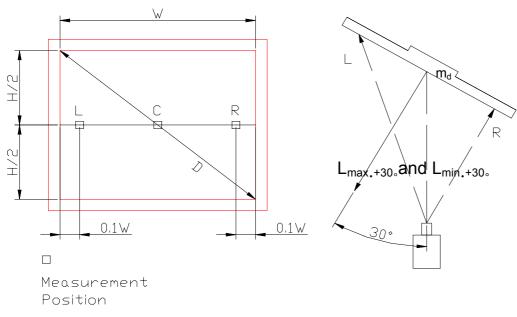
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Note 3: TCO '99 Certification Requirements and test methods for environmental labeling of Display Report No. 2 defines Luminance uniformity as below:

((Lmax,+30deg. / Lmin,+30deg.) + (Lmax,-30deg. / Lmin,-30deg.)) / 2

TCO 99 certification requirements and test methods

1) Diagrammatic •



Effective area • W×H

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2) D is the diagonal of the screen in centimeters. And m_d is as follow.

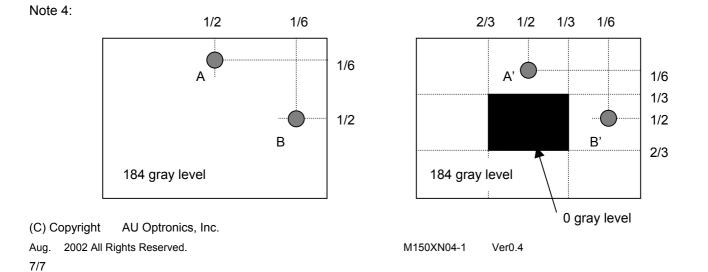
 $m_d(cm)=D\times1.5$ with a minimum distance $\cdot m_d=50$ cm

3) The measurement positions shall consist of an active white square of size 4 by 4 cm.

4)Measure $L_{max,+30}$ and $L_{min,+30}$ that are given as points L & R. And then rotated to an azimuth angle of -30 degrees and measure $L_{max,-30}$ and $L_{min,-30}$ again using the same procedure.

5)The variation in luminance ratio is calculated as the ratio:

 $L_{R} = ((L_{max.+30^{\circ}}/L_{min.+30^{\circ}}) \cdot (L_{max.-30^{\circ}}/L_{min.-30^{\circ}}))/2 \cdot \underline{1.7}$

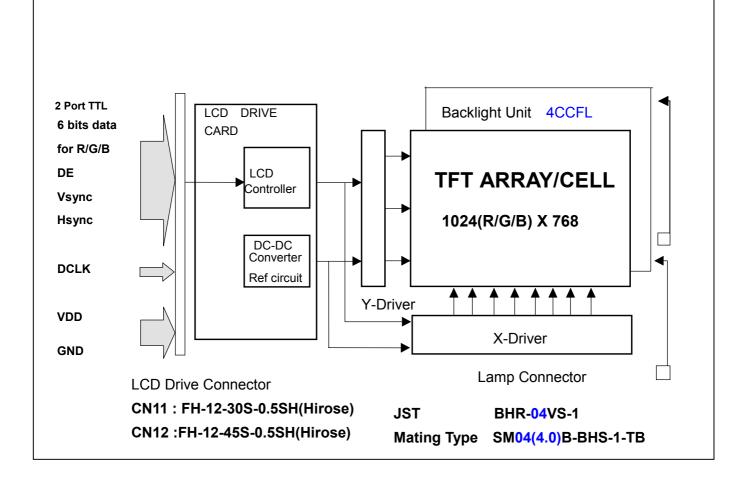


Unit: percentage of dimension of display area

 $I L_A-L_{A'} I / L_A x 100\%$ = 1.2% max., L_A and L_B are brightness at location A and B $I L_B-L_{B'} I / L_B x 100\%$ = 1.2% max., $L_{A'}$ and $L_{B'}$ are brightness at location A' and B'

2.2 Functional Block Diagram

The following diagram shows the functional block of 15.0 inches Color TFT-LCD Module:

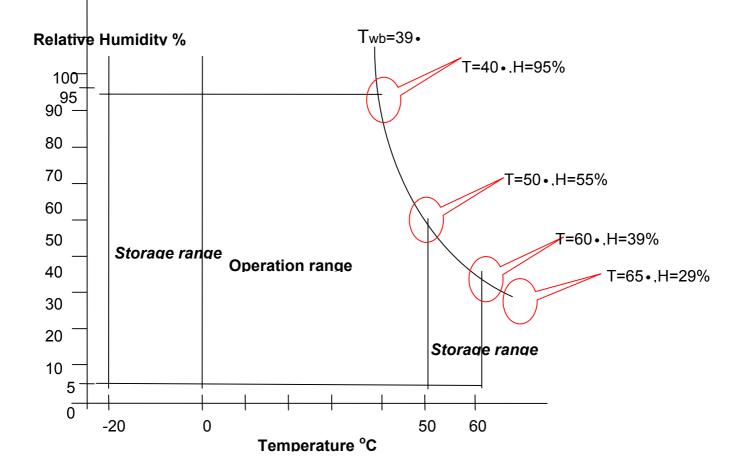


3.0 Absolute Maximum Ratings

ltem	Symbol	Min	Max	Unit	Conditions
Logic/LCD Drive Voltage	VDD	-0.3	+3.6	[Volt]	
Input Voltage of Signal	Vin	-0.3	VDD+0.3	[Volt]	
CCFL Current	ICFL	-	9.0	[mA] rms	Note 1
Operating Temperature	TOP	0	+50	[°C]	Note 2
Operating Humidity	HOP	20	85	[%RH]	Note 2
Storage Temperature	TST	-20	+60	[°C]	Note 2
Storage Humidity	HST	5	95	[%RH]	Note 2
Vibration			1.5 / 10-200	[G / Hz]	
Shock			50/20	[G / ms]	Half sine wave
Assured Torque at Side Mount			2.0	[kgf.cm]	
Re-screw			3	[Times]	

Absolute maximum ratings of the module is as following:

Note 1:To exceed 5.5mA, life time accelerate drop down and if to exceed 9.0mA has safety problem Note 2: Maximum Wet-Bulb should be 39• and No condensation. Wet bulb temperature chart



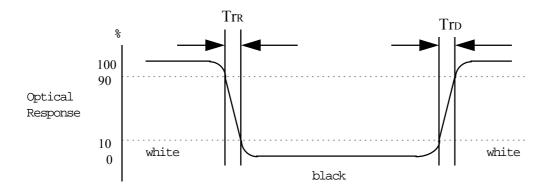
4.0 Optical Characteristics

The optical characteristics are measured under stable conditions at 25. (Room Temperature):

Item	Unit	Conditions	Min.	Тур.	Max.
		Horizontal (Right)			
		CR = 10 (Left)	65	70	
Viewing Angle	[degree]		65	70	
	[degree]	Horizontal (Right)	70	80	
		CR = 10 (Left)	70	80	
		Vertical (Upper)	50	60	
CR: Contrast Ratio	[degree]	CR = 10 (Lower)	50	60	
	[degree]	Vertical (Upper)	60	70	
		CR = 5 (Lower)	70	80	
Contrast ratio		Normal Direction	300	400	-
Response Time(Note 1)	[msec]	Raising Time Ton (10%-90%)	-	5	9
	[msec]	Falling Time Toff (90%-10%)	-	11	15
	[msec]	Raising + Falling	-	16	24
Color / Chromaticity		Red x	0.59	0.62	0.65
Coordinates (CIE)		Red y	0.31	0.34	0.37
		Green x	0.27	0.30	0.33
		Green y	0.56	0.59	0.62
		Blue x	0.12	0.15	0.18
		Blue y	0.07	0.1	0.13
Color Coordinates (CIE) White		White x	0.28	0.31	0.34
		White y	0.3	0.33	0.36
Brightness Uniformity	[%]		75	80	-
White Luminance at CCFL 5.5mA	[cd/m ²]		460	500	_
Crosstalk (in 75Hz)	[%]				1.2

Note 1: Definition of Response time:

The output signals of photodetector are measured when the input signals are changed from "Black" to "White" (falling time), and from "White" to "Black" (rising time), respectively. The response time interval between the 10% and 90% of amplitudes. Refer to figure as below.



5.0 Signal Interface

5.1 Module Interface Connectors

Physical interface is described as for the connector on module.

These connectors are capable of accommodating the following signals and will be following components

Connector Name	Interface Connector
Manufacturer	Hirose or compatible
Type / Part Number	CN11 : FH-12-30S-0.5SH (Hirose)
	CN12 : FH-12-45S-0.5SH (Hirose)

5.2 Module Connector Pin Configuration

CN11 : FH-12-30S-0.5SH (Hirose)		CN12 : FH-12-45S-0.5SH (Hirose)			
Pin No.	Symbol	Description	Pin No.	Symbol	Description
1	GND	ground	1	GND	ground
2	BE5	Blue even data(MSB)	2	CLK	Data clock
3	BE4	Blue even data	3	GND	ground
4	BE3	Blue even data	4	DENA	Data enable
5	BE2	Blue even data	5	GND	ground
6	GND	ground	6	VD	Vertical sync
7	BE1	Blue even data	7	GND	ground
8	BE0	Blue even data(LSB)	8	HD	Horizontal sync
9	NC	reserve	9	GND	ground
10	NC	reserve	10	GND	ground
11	GND	ground	11	GND	ground
12	GE5	Green even data(MSB)	12	BO5	Blue odd data(MSB)
13	GE4	Green even data	13	BO4	Blue odd data
14	GE3	Green even data	14	BO3	Blue odd data

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15	GE2	Green even data	15	BO2	Blue odd data
16	GND	ground	16	GND	ground
17	GE1	Green even data	17	BO1	Blue odd data
18	GE0	Green even data(LSB)	18	BO0	Blue odd data(LSB)
19	NC	reserve	19	NC	reserve
20	NC	reserve	20	NC	reserve
21	GND	ground	21	GND	ground
22	RE5	Red even data(MSB)	22	BO5	Green odd data(MSB)
23	RE4	Red even data	23	BO4	Green odd data
24	RE3	Red even data	24	BO3	Green odd data
25	RE2	Red even data	25	BO2	Green odd data
26	GND	ground	26	GND	ground
27	RE1	Red even data	27	BO1	Green odd data
28	RE0	Red even data(LSB)	28	BO0	Green odd data(LSB)
29	NC	reserve	29	NC	reserve
30	NC	reserve	30	NC	reserve
			31	GND	ground
			32	RO5	Red odd data(MSB)
			33	RO4	Red odd data
			34	RO3	Red odd data
			35	RO2	Red odd data
			36	GND	ground
			37	RO1	Red odd data
			38	RO0	Red odd data(LSB)
			39	NC	reserve
			40	NC	reserve
			41	VCC	Power input +3.3V
			42	VCC	Power input +3.3V
			43	VCC	Power input +3.3V
			44	NC	reserve
			45	NC	reserve

5.3 Backlight Connectors

Connector Name / Designation	For Lamp Connector
Manufacturer	JST or compatible
Type / Part Number	BHR-04VS-1

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Mating Type / Part Number	SM04(4.0)B-BHS-1-TB
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5.4 Backlight Connector Pin Configuration

Pin	Symbol	Description
1	HV	Lamp High Voltage
2	HV	Lamp High Voltage
3	NC	No connection
4	LV	Ground

- Cable length: 150 +- 5 mm
- Connector-output position: right side(front view)
- Lamp assy design shall be easy for replacement and repair.

5.5 Signal Electrical Characteristics

Each signal characteristics are as follows;

ltem	Symbol	Min	Тур	Мах	Unit
LCD Drive voltage	VDD	+3.0	+3.3	+3.6	[V]
"High" input signal voltage	Vih	2.0	-	-	[V]
"Low" input signal voltage	Vil	-	-	0.8	[V]

5.6 Interface Timings Characteristics

Signal	ltem	Symbol	MIN	TYP	MAX	Unit
DCLK	Frequency	1/TDCLK	-	32.5	40.0	MHz
	Period	TDCLK	25	30.8	-	ns
	High time	TCH	0.4	0.5	0.6	TDCLK
	Low time	TCL	0.4	0.5	0.6	TDCLK
DATA	Setup time	TDS	3	-	-	ns
	Hold time	TDH	1	-	-	ns
Data Enable	Setup time	TES	3	-	-	ns
	Hold time	TEH	1			ns
Horizontal sync	Frequency	1/TH	-	48	60	KHz
-	Pulse width	THP	2	68	-	TDCLK
Horizontal	Back –porch	THB	1	80	-	TDCLK
Signal	Display period	THD	512	512	512	TDCLK
-	Front-porch	THF	0	12	-	TDCLK
	H total	TH	600	672		
Vertical sync	Frequency	1/TV	-	60	75	Hz
	Pulse width	TVP	1	6	-	TH
Vertical	Back-porch	TVB	7	29	64	TH
Signal	Display period	TVD	768	768	768	TH
-	Front-porch	TVF	1	3	-	TH

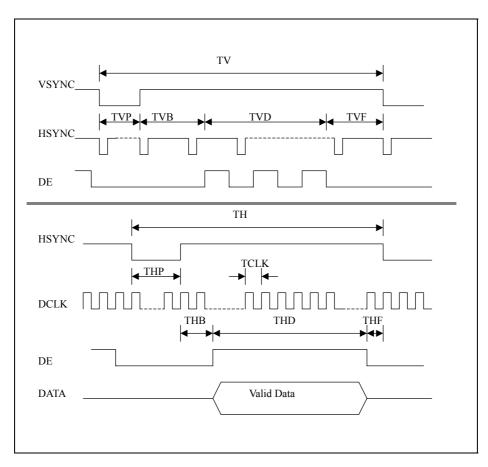
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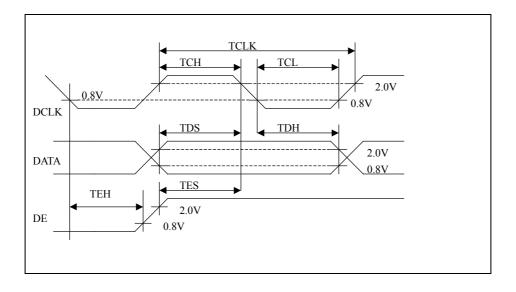
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Vs	sync period +	TVP+TVB	8	64	
Vb	back-porch				

5.7 Interface Timing Definition

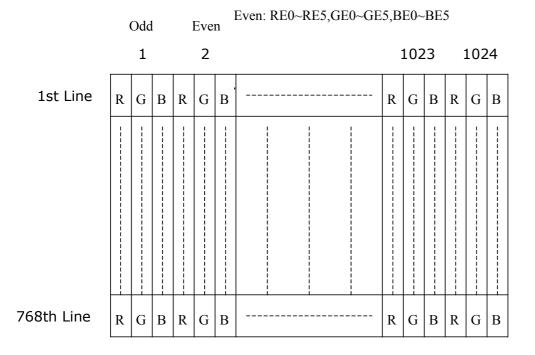




6.0 Pixel format image

Following figure shows the relationship of the input signals and LCD pixel format.

Odd:RO0~RO5,GO0~GO5,BO0~BO5



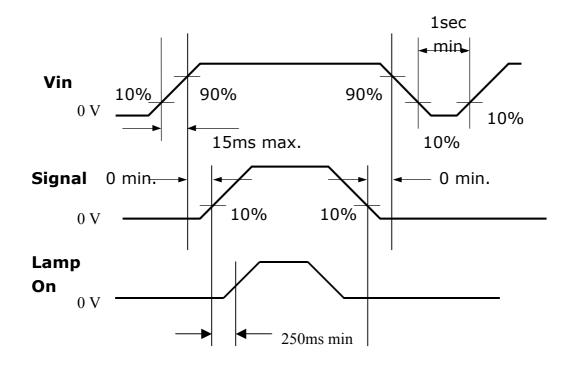
7.0 Power Consumption

Input power specifications are as follows;

Symbol	Parameter	Min	Тур	Мах	Units	Condition
VDD	LCD Drive Voltage	3.0	3.3	3.6	[V]	
IDD	LCD Drive Current	-	1000	1150	[mA]	VDD=3.3v, All Black Pattern
PDD	LCD Drive power consumption	-	3.3	3.8	[Watt]	VDD=3.3v, All Black Pattern
VDDns	Allowable LCD Drive Ripple Noise			100	[mV] p-p	

8.0 Power ON/OFF Sequence

VDD power and lamp on/off sequence is as follows. Interface signals are also shown in the chart.



9.0 **Backlight Characteristics**

9.1 Signal for Lamp connector

Pin #	signal Name
1	Lamp High Voltage
2	Lamp High Voltage
3	No connection
4	Ground

9.2 Parameter guide line for CCFL Inverter

Symble	Parameter	Min	Тур	Мах	Units	Condition
IRCFL	CCFL operation range	3.0	5.5	8.5	[mA] rms	(Ta=25•) Note 4
ICFL	CCFL Inrush current	-	-	20	[mA]	
fCFL	CCFL Frequency	40	55	60	[KHz]	(Ta=25•) Note 1
ViCFL (25•) (reference)	CCFL Ignition Voltage	1000			[Volt] rms	(Ta= 25•) Note 3
ViCFL (0•) (reference)	CCFL Ignition Voltage	1300			[Volt] rms	(Ta= 0•) Note 3
VCFL	CCFL Discharge Voltage (Reference)		680	725	[Volt] rms	(Ta=25•) Note 2
PCFL	CCFL Power consumption @ 5.5mA (excluding inverter)		15	16	[Watt]	(Ta=25•) Note 2

Note 1: CCFL Frequency should be carefully determined to avoid interference between inverter and TFT LCD Note 2: Calculator value for reference (ICFL×VCFLx4=PCFL)

- Note 3: CCFL inverter should be able to give out a power that has a generating capacity of over 1300 voltage. Lamp units need 1300 voltage minimum for ignition
- Note 4: CCFL life time 50,000hr under 8mA, it's defined as when the brightness is reduced by half. To exceed 8.0mA, life time accelerate drop down and if to exceed 8.5mA has safety problem.

10.0 Vibration, Shock, and Drop

10.1 Vibration & Shock

The module shall work error free after following vibration and shock condition. Likewise the module shall not sustain any damage after vibration and shock test.

10.1.1 Vibration Test Spec:

- Frequency:10 200Hz
- Sweep: 30 Minutes each Axis (X, Y, Z)
- Acceleration: 1.5G(10~200Hz P- P)
- Test method:

Acceleration (G)	1.5
Frequency (Hz)	10~200~10
Active time(min)	30

10.1.2 Shock Test Spec:

Acceleration (G)	50
Active time(ms)	20
Wave form	half-sin
Times	1

• Direction: $\pm X$, $\pm Y$, $\pm Z$

10.2 Drop

Package test: The drop height is defined as 60 cm.

(1 corner,3 edges,6 flat faces)

-- No damage and defect found for panel.

11.0 Environment

The display module will meet the provision of this specification during operating condition or after storage or shipment condition specified below. Operation at 10% beyond the specified range will not cause physical damage to the unit.

11.1 Temperature and Humidity

11.1.1 Operating Conditions

The display module operates error free, when operated under the following conditions;

Temperature	0 °C to 50 °C
Relative Humidity	20% to 85%
Wet Bulb Temperature	39.0 ^⁰ C

11.1.2 Shipping Conditions

The display module operates error free, after the following conditions;Temperature-20 $^{\circ}$ C to 60 $^{\circ}$ CRelative Humidity5% to 95%Wet Bulb Temperature39.0 $^{\circ}$ C

11.2 Atmospheric Pressure

The display assembly is capable of being operated without affecting its operations over the pressure range as following specified:

	Pressure	Altitude
Maximum Pressure	1040 hPa	0 m = sea level
Minimum Pressure	601 hPa	3658 m = 12,000 feet

Note: Non-operation altitude limit of this display module = 40,000 feet. = 12193 m.

11.3 Thermal Shock

The display module will not sustain damage after being subjected to 100 cycles of rapid temperature change. A cycle of rapid temperature change consists of varying the temperature from -20° C to 60° C, and back again.

Thermal shock cycle-20 °C for 30min60 °C for 30min

Power is not applied during the test. After temperature cycling, the unit is placed in normal room ambient for at least 4 hours before powering on.

12.0 Reliability

This display module and the packaging of that will comply following standards.

12.1 Failure Criteria

The display assembly will be considered as failing unit when it no longer meets any of the requirements stated in this specification. Only as for maximum white luminance, following criteria is applicable.

• Maximum white Luminance shall be 75 cd/m² or more.

12.2 Failure Rate

The average failure rate of the display module (from first power-on cycle till 1,000 hours later) will not exceed 1.0%. The average failure rate of the display module from 1,000 hours until 10,000 hours will not exceed 0.70% per 1000 hours.

12.2.1 Usage

The assumed usage for the above criteria is:

- 220 power-on hours per month
- 500 power on/off cycles per month
- Maximum brightness setting
- Operation to be within office environment (25[°]C typical)

12.2.2 Component De-rating

All the components used in this device will be checked the load condition to meet the failure rate criteria.

12.3 CCFL Life

The assumed CCFL Life will be longer than 30,000 hours under stable condition at 25 ± 5 ⁰C; Standard current at 8.0 \pm 0.5 mA Definition of life: brightness becomes 50% or less than the minimum luminance value of CCFL.

12.4 ON/OFF Cycle

The display module will be capable of being operated over the following ON/OFF Cycles.

ON/OFF	Value	Cycle
+VDD and CCFL power	36,000	10 seconds on / 10 seconds off

13.0 Safety

13.1 Sharp Edge Requirements

There will be no sharp edges or corners on the display assembly that could cause injury.

13.2 Materials

13.2.1 Toxicity

There will be no carcinogenic materials used anywhere in the display module. If toxic materials are used, they will be reviewed and approved by the responsible AUO Toxicologist.

13.2.2 Flammability

All components including electrical components that do not meet the flammability grade UL94-V1 in the module will complete the flammability rating exception approval process. The printed circuit board will be made from material rated 94-V1 or better. The actual UL flammability rating will be printed on the printed circuit board.

14.0 Other requirements

14.1 Smoke Free Design

By any single failure, any smoke nor strange smell shall not be observed by the operator.

14.2 National Test Lab Requirement

The display module will satisfy all requirements for compliance to the following requirement:

UL 1950, First Edition	U.S.A. Information Technology Equipment
CSA C22.2 No.950-M89	Canada, Information Technology Equipment
IEC 950	International, Information Technology Equipment
EN 60 950	International, Information Processing Equipment
	(European Norm for IEC950)

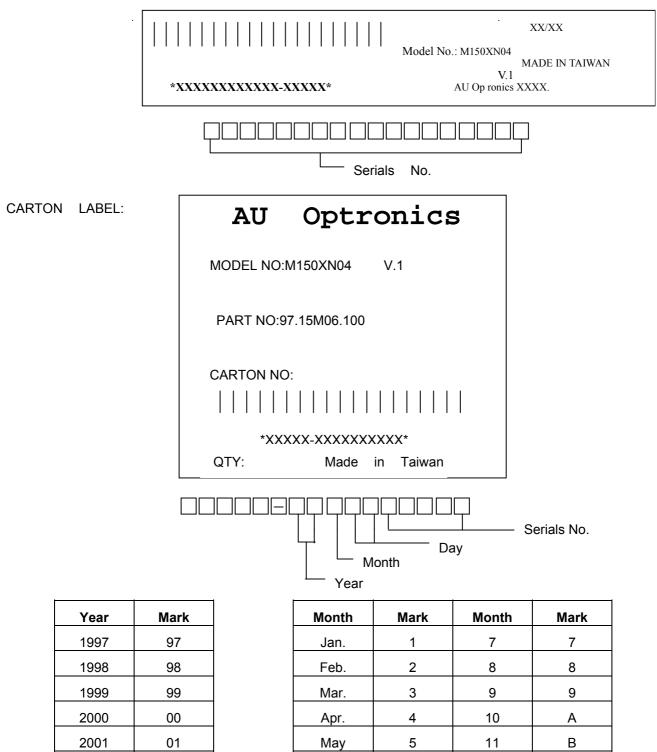
The construction of the display module is designed to suppress EMI. When mounted into a specified host system, the system will meet the following EMI requirement:

FCC Part 15 Class B VCCI Class 2 CISPR 22, class B Taiwan CNS standard China CCIB standard

15.0 Label

15.1 Label

Unit mark is printed on a label. The label is shown as below:



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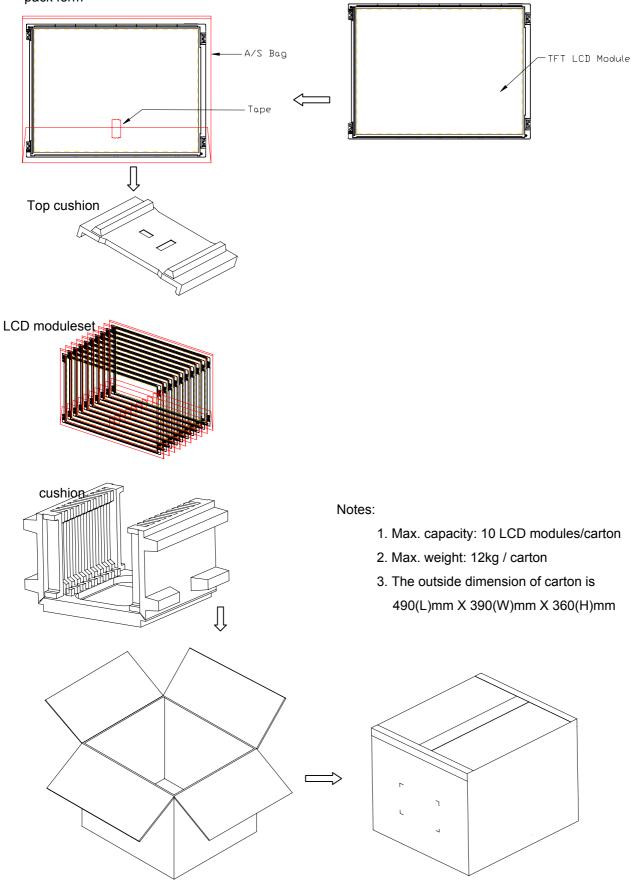
M150XN04-1 Ver0.4

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16.Packing

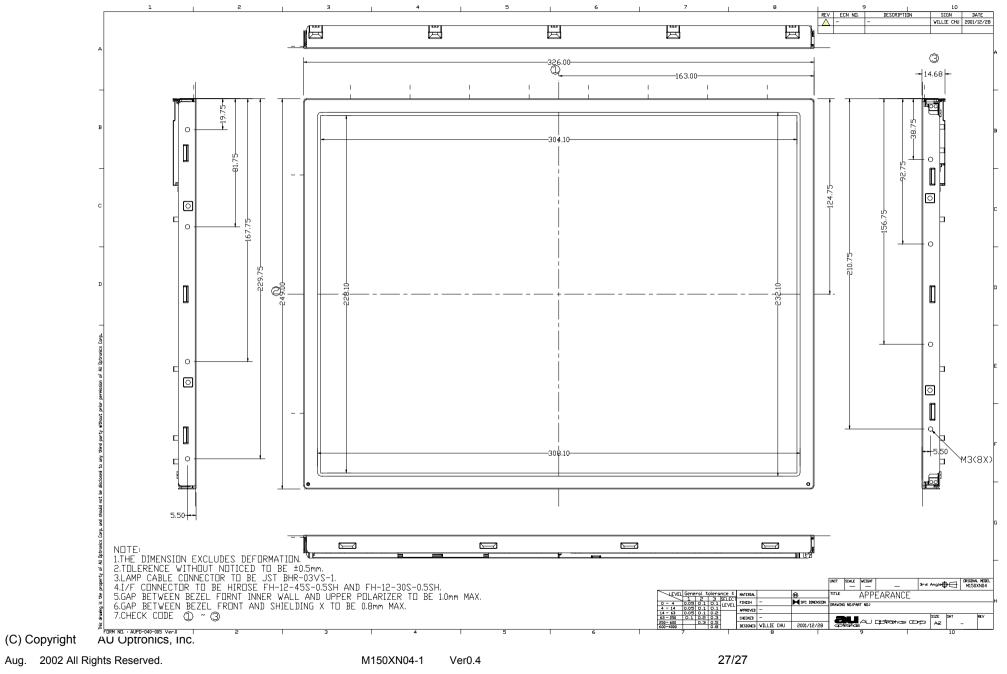
pack form



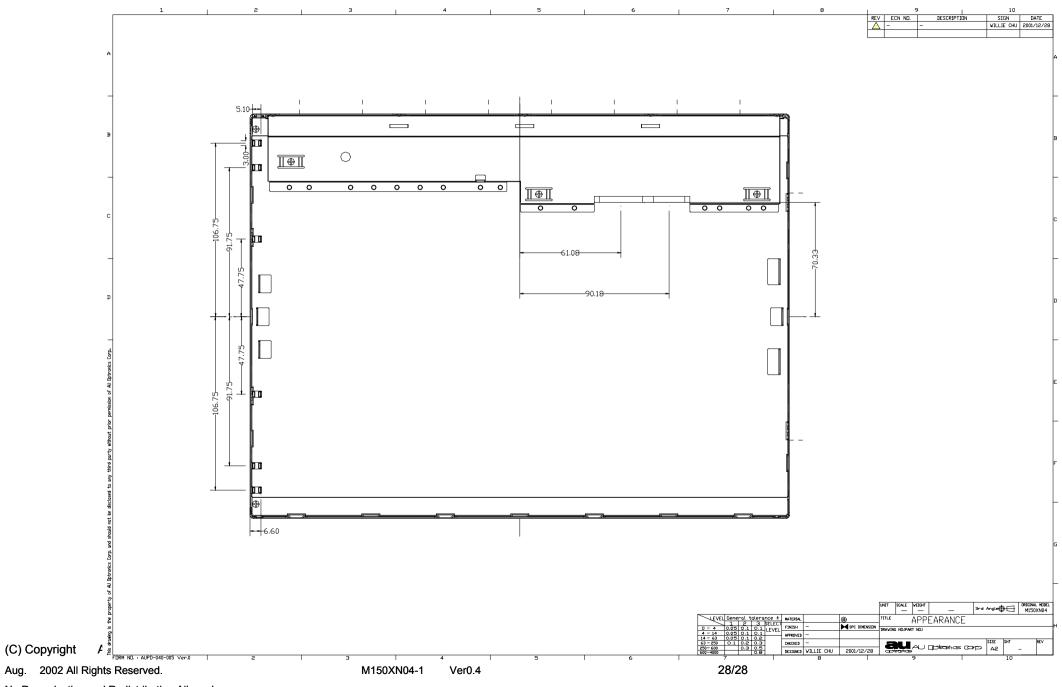
10(CCDoMoigutes/Cartoptboxics, Inc.

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