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Product Specification

6.5" color TFT-LCD module

MODEL NAME: A065GW01

(•) Preliminary Specification
() Final Specification

Official UK Representative



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Please verify this is the latest information. E&OE $\,$

Note: The content of this specification is subject to change.

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Record of Revision

		1	Record of Revision
Version	Revise Date	Page	Content
0	18/Feb./2002	0	First draft.
1	20/June./2001	1	Polarizer treatment: LR(1.2%) → without LR treatment
		1	Outline: 155(W)×90(H)×6.94(D) → 157.2(W)×89.8(H)×7.8 (D)
		5	Storage temperature: -30 ~95°C →-40 ~95°C
		6	Lamp driving conditions: 6.5mA → 6mA
		10	Humidity: 95% → 90%
		12~13	Revise the mechanical drawing
2	11/Nov./2002	3	155g(typ)→142g(typ)
		5	Lamp Voltage: V _L : 500 Vrms(typ)→520Vrms(typ)
		8	Brightness test condition: -20°C, I _L from 11mA to 9.5 mA
		8	Brightness: -10 °C, 190nit (typ) →230nit (typ),
			100nit (min) →190nit (min)
		8	Brightness: -20 ℃, 180nit (typ), 150nit (min)
		8	Response Time : Tr:25ms→15ms, Tf:30ms→20ms.
		11	Revised Packing Form
3	26/Feb./2003	12	Revised the product drawing
4	04/Nov./2003	5	Operation Temperature changed from -30 ~70 $^\circ\!$
5	13/Feb./2004		Change AUO CIS system
		12	Added FPC drawing
L	L	l	



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A. Physical specifications

NO.	ltem	Specification	Remark
1	Display resolution(dot)	400RGB(W)×234(H)	
2	Active area(mm)	143.4W)×79.326(H)	
3	Screen size(inch)	6.5(Diagonal)	
4	Dot pitch(mm)	0.119.5(W)×0.339 (H)	
5	Color configuration	R. G. B. stripe	
6	Overall dimension(mm)	157.2(W)×89.8(H)×7.8(D)	Note 1
7	Weight(g)	142±10	
8	Surface treatment	AG(5.5% haze) & with WV film	
9	Backlight unit	L type lamp	

Note 1: Refer to Fig. 1



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B. Electrical specifications

1.Pin assignment

a. TFT-LCD panel driving section

Pin no	Symbol	I/O	Description	Remark
1	GND	-	Ground for logic circuit	
2	V _{CC}	I	Supply voltage of logic control circuit for scan driver	
3	V_{GL}	ı	Negative power for scan driver	
4	V_{GH}	I	Positive power for scan driver	
5	STVR	I/O	Vertical start pulse	Note 1
6	STVL	I/O	Vertical start pulse	Note 1
7	CKV	I	Shift clock input for scan driver	
8	U/D	I	UP/DOWN scan control input	Note 1,2
9	OEV	I	Output enable input for scan driver	
10	VCOM	I	Common electrode driving signal	
11	VCOM	I	Common electrode driving signal	
12	L/R	I	LEFT/RIGHT scan control input	Note 1,2
13	MOD	ı	Sequential sampling and simultaneous sampling setting	Note 3
14	OEH	I	Output enable input for data driver	
15	STHL	I/O	Start pulse for horizontal scan line	Note 1
16	STHR	I/O	Start pulse for horizontal scan line	Note 1
17	CPH3	I	Sampling and shifting clock pulse for data driver	
18	CPH2	I	Sampling and shifting clock pulse for data driver	
19	CPH1	I	Sampling and shifting clock pulse for data driver	
20	V _{CC}	I	Supply voltage of logic control circuit for data driver	
21	GND	-	Ground for logic circuit	
22	VR	I	Alternated video signal input(Red)	
23	VG	I	Alternated video signal input(Green)	
24	VB	I	Alternated video signal input(Blue)	
25	AV_{DD}	I	Supply voltage for analog circuit	
26	AV _{SS}	-	Ground for analog circuit	

Note 1: Selection of scanning mode (please refer to the following table)

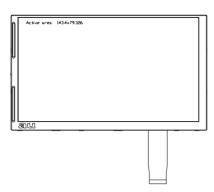
riote in delection of economic mede (predect ferente the ferente thing table)									
Setting of scan control input				Γ state t pulse		Scanning direction			
U/D	L/R	STVR	STVL	STHR	STHL				
GND	V _{CC}	OUT	IN	OUT	IN	From up to down, and from left to right.			
V _{CC}	GND	IN	OUT	IN	OUT	From down to up, and from right to left.			
GND	GND	OUT	IN	IN	OUT	From up to down, and from right to left.			
V _{CC}	V _{cc}	IN	OUT	OUT	IN	From down to up, and from left to right.			

IN: Input; OUT: Output.



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Note 2: Definition of scanning direction. Refer to figure as below:



Note 3: MOD = H: Simultaneous sampling. MOD = L: Sequential sampling. Please set CPH2 and CPH3 to GND when MOD = H.

b. Backlight driving section (Refer to Fig. 1)

No.	Symbol	1/0	Description	Remark
1	HI		Power supply for backlight unit (High voltage)	
2	GND	ı	Ground for backlight unit	

2. Absolute maximum ratings

Item	Symbol	Condition	Min.	Max.	Unit	Remark
	V_{CC}	GND=0	-0.3	7	V	
	AV_{DD}	AV _{SS} =0	-0.3	7	V	
Power voltage	V_{GH}	0.110	-0.3	18	V	
	V_{GL}	GND=0	-15	0.3	V	
	$V_{GH} - V_{GL}$		•	33	V	
	V_{i}		-0.3	AV _{DD} +0.3	V	Note 1
Input signal voltage	VI		-0.3	V _{CC} +0.3	V	Note 2
	VCOM		-2.9	5.2	V	

Note 1: VR, VG, VB.

Note 2: STHL, STHR, OEH, L/R, CPH1~CPH3, STVR, STVL, OEV, CKV, U/D.

3. Environment condition

	Item	Condition	Spec.	Remark
Operating	Panel with	Ambient	-30 ~85°C	Note 1,2,3
temperature	lighting BLU	(panel surface)		
Storage	Panel with	Surface	-40 ~95°C	Note 4
temperature	lighting BLU	temperature		
	Panel	Surface	-40 ~95°C	
		temperature		

Note 1:Under the condition of the operating temperature, the panel would be function normal for the visual display only. For contrast, response time, and other factors related to display quality, determine temperature using the formula Ta=25°C

Note 2: Panel surface temperature is defined as the maximum temperature of panel surface. In general, the lamp side temperature is higher than non-lamp side around 10 $^{\circ}$ C

Note 3: The backlight has been turned on before running about condition.

Note 4: BLU is off



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4. Electrical characteristics

a. Typical operating conditions (GND=AVss=0V, Note 4)

Item)	Symbol	Min.	Тур.	Max.	Unit	Remark
		Vcc	3	5	5.2	V	
		AV_DD	4.8	5	5.2	V	
Power su	upply	V_{GH}	14.3	15	15.7	V	
		V_{GL}	-10.5	-10	-9.5	V	
Video si	•	V_{iA}	0.4	-	AV _{DD} -0.4	V	Note 1
amplitu		V_{iAC}	-	3	-	V	AC component
(VR,VG,	, V D)	V_{iDC}	-	AV _{DD} /2	-	V	DC component
\/COI		V_{CAC}	3.5	5.6	6.5	Vp-p	AC component, Note 2
VCOM		V_{CDC}	(1.4)	(1.7)	(2.0)	V	DC component
Input	H Level	V_{IH}	0.8V _{CC}	-	V _{CC}	V	Note 2
signal voltage	L Level	V_{IL}	0	-	0.2V _{CC}	V	Note 3

Note 1: Refer to Fig.4- (a).

Note 2: The brightness of LCD panel could be changed by adjusting the AC component of VCOM.

Note 3: STHL, STHR, OEH, L/R, CPH1~CPH3, STVR, STVL, OEV, CKV, U/D.

Note 4: Be sure to apply GND, V_{CC} and V_{GL} to the LCD first, and then apply V_{GH}.

b. Current consumption (GND=AVss=0V)

Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
Current	I_{GH}	V _{GH} =15V	-	0.20	0.5	ΜA	
for driver	I_{GL}	V _{GL} =-10V	-	0.80	1.5	mA	
anver	I _{CC}	V _{CC} =5V	-	3.0	6.0	mA	
	I _{DD}	AV _{DD} =5V	-	17.0	30	mA	

c. Backlight driving conditions

Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
Lamp voltage	V_L	-	520	550	Vrms	
Lamp current	Ι _L	1	6	6.5	mArms	
Frequency	F_L	-	60	80	kHz	Note 4
Lamp starting	Vs	-	-	850	Vrms	Note 1,5
Lamp starting voltage		-	-	1150	Vrms	Note 2,5
vollago		-	-	1300	Vrms	Note 3,5
Lamp life time		(25000)	(40000)	-	Hr	Note 6

Note 1: Ta = 25° C.

Note 2: Ta = 0° C.

Note 3: Ta = -30° C.

Note 4: The lamp frequency should be selected as different as possible from display horizontal synchronous signal to avoid interference.

Note 5: For starting the backlight unit, the output voltage of DC/AC's transformer should be larger than the maximum lamp starting voltage.



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Note 6: The" Lamp life time" is defined as the module brightness decrease to 50% original brightness at Ta=25 $^{\circ}$ C , I_L=6mA.

5. AC Timing

a. Timing conditions

Parameter	Symbol	Min.	Тур.	Max.	Unit.	Remark
Rising time	t _r	-	-	10	ns	Note 1
Falling time	t_{f}	-	-	10	ns	Note 1
High and low level pulse width	t _{CPH}	99	103	107	ns	CPH1~CPH3
CPH pulse duty	t_{CWH}	40	50	60	%	CPH1~CPH3
CPH pulse delay	t _{C12} t _{C23} t _{C31}	30	t _{CPH} /3	t _{CPH} /2	ns	CPH1~CPH3
STH setup time	t _{SUH}	20	-	-	ns	STHR,STHL
STH hold time	t _{HDH}	20	-	-	Ns	STHR,STHL
STH pulse width	t _{STH}	ı	1	-	t _{CPH}	STHR,STHL
STH period	t _H	61.5	63.5	65.5	μs	STHR,STHL
OEH pulse width	t _{OEH}	ı	1.22	-	μs	OEH
Sample and hold disable	t _{DIS1}	ı	8.28	-	μs	
OEV pulse width	t_{OEV}	ı	5.40	-	μs	OEV
CKV pulse width	t _{CKV}	-	4.18	-	μ s	CKV
Clean enable time	t _{DIS2}	-	3.74	-	μ s	
Horizontal display start	t_{SH}	-	0	-	T _{CPH} /3	
Horizontal display timing range	t _{DH}	-	1200	-	T _{CPH} /3	
STV setup time	t _{SUV}	400	-	-	ns	STVL,STVR
STV hold time	t_{HDV}	400	-	-	ns	STVL,STVR
STV pulse width	t _{STV}	-	-	1	t _H	STVL,STVR
Horizontal lines per field	t_{V}	256	262	268	t _H	Note 2
Vertical display start	t _{sv}		3	-	t _H	
Vertical display timing range	t _{DV}		234	-	t _H	
VCOM rising time	t _{rCOM}		-	5	μ s	
VCOM falling time	t _{fCOM}		-	5	μ s	
VCOM delay time	t _{DCOM}		-	3	μ s	
RGB delay time	t _{DRGB}		-	1	μs	

Note 1: For all of the logic signals.

Note 2: Please don't use odd horizontal lines to drive LCD panel for both odd and even field simultaneously.

b. Timing diagram

Please refer to the attached drawing, from Fig.2 to Fig.6.

C. Optical specification (Note 1)



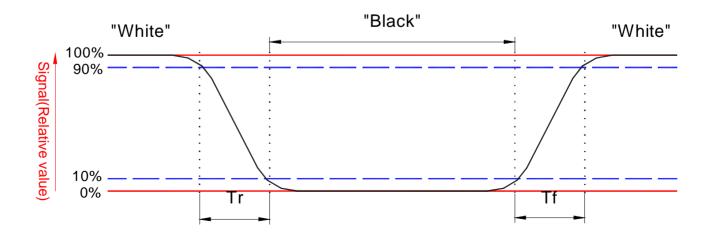
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Item	1	Symb	Condition	Min.	Тур.	Max.	Unit	Remark
Response time	Rise Fall	Tr Tf	<i>θ</i> =0°	1 1	15 20	50 60	ms ms	Note 2,4
Contrast	ratio	CR	At optimized Viewing angle	100	300	-		Note 3,4
	Top Bottom Left Right		CR≧10	20 40 45 45	40 55 60 60	- - -	deg.	Note 4,6
Viewing angle	Top Bottom Left Right		CR≧5	30 60 60 60	50 70 70 70	- - -	deg.	Note 4,5
Brightne	ess	Y_L	I _L =6mA, 25°ℂ	400	500	-	nit	Note 6
			I _L =9.5mA, -10 °ℂ, 1min	190	230		nit	
			I _L =9.5mA, -20 ℃, 1min	150	180	-	nit	
VA/Initial alamana (* 11		Х	<i>θ</i> =0°	0.26	0.31	0.36		Note 6
White chror	пансну	Υ	$\theta = 0^{\circ}$	0.28	0.33	0.38		

Note 1 : Ambient temperature =25 $^{\circ}$ C. And lamp current I_L = 6 mArms. To be measured in the dark room and to be measured on the center area of panel with a viewing cone of 1 $^{\circ}$ by Topcon luminance meter BM-5, after 10 minutes operation.

Note 2. Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to figure as below.





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Note 3. Definition of contrast ratio:

Contrast ratio is calculated with the following formula.

Contrast ratio (CR)= Photo detector output when LCD is at "White" state

Photo detector output when LCD is at "Black" state

Note 4. White $Vi=V_{i50} + 1.5V$

Black Vi=V_{i50} ± 2.0V

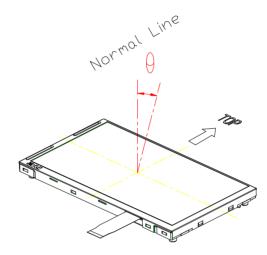
"±" means that the analog input signal swings in phase with V_{COM} signal.

" $\overline{+}$ " means that the analog input signal swings out of phase with V_{COM} signal.

V_{i50}: The analog input voltage when transmission is 50%

The 100% transmission is defined as the transmission of LCD panel when all the input terminals of module are electrically opened.

Note 5. Definition of viewing angle, refer to figure as below.



Note 6. Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.

Note 7. It is combine with application circuit which AU Optronics recommendation.



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D. Reliability test items(Note 2):

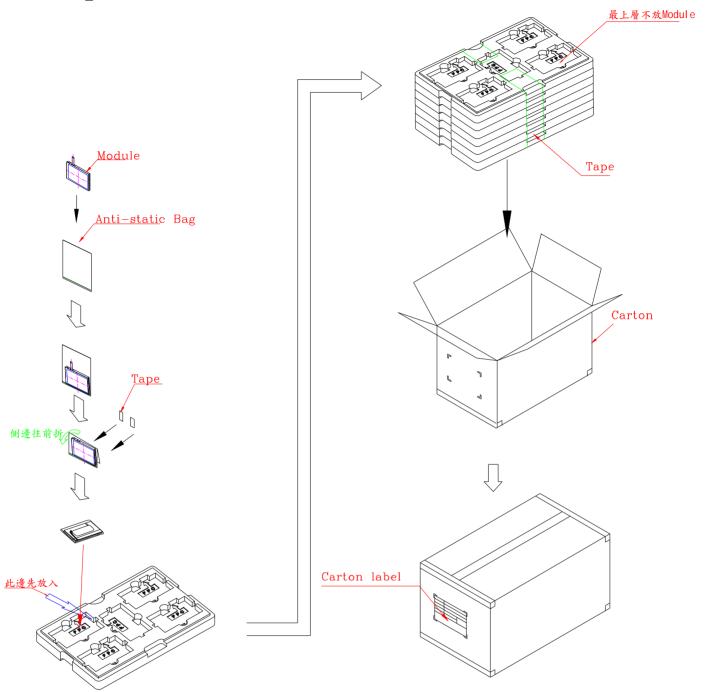
No.	Test items	Conditions	Remark		
1	High temperature storage	Ta= 95°C 240Hrs			
2	Low temperature storage	Ta= -40°C 240Hrs			
3	High temperature operation	Ta= 85°C 240Hrs			
4	Low temperature operation	Ta= -30°C 240Hrs			
5	High temperature and high humidity	Ta= 60℃, 90% RH 240Hrs	Operation		
6	Heat shock	-30°C ~85°C /200 cycles 1Hrs/cycle	Non-operatio		
7	Electrostatic discharge	\pm 200V,200pF(0 Ω), once for each terminal	Non-operatio		
8	Vibration	Frequency range : 10~55Hz Stoke : 1.5mm Sweep : 10 ~ 55 ~ 10Hz 2 hours for each direction of X,Y,Z (6 hours for total)	JIS C7021, A-10 condition A		
9	Mechanical shock	JIS C7021, A-7 condition C			
10	Vibration (with carton)	Random vibration: 0.015G²/Hz from 5~200Hz -6dB/octave from 200~500Hz			
11	Drop (with carton)	Height: 60cm 1 corner, 3 edges, 6 surfaces	JIS Z0202		

Note 1: Ta: Ambient temperature.

Note 2: In the standard conditions, there is not display function NG issue occurred. All the cosmetic specification is judged before the reliability stress.



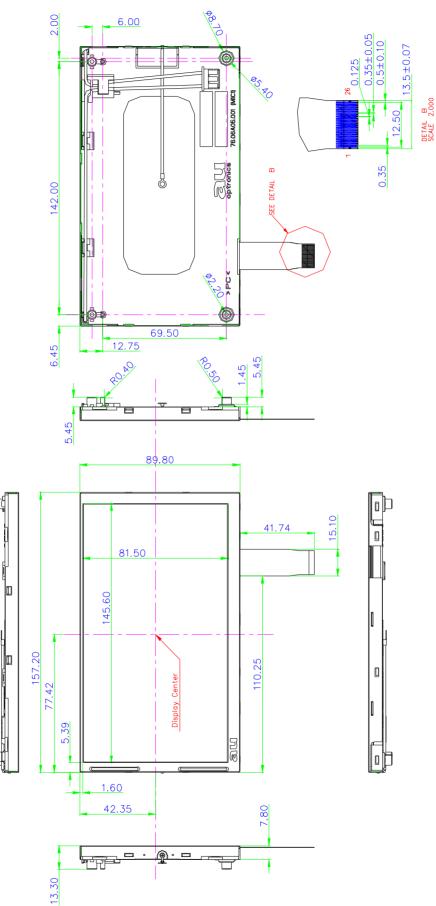
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Max. Capacity: 30 Modules Max. Weight: 7 Kg Carton outline.: 520mm*340mm*250mm

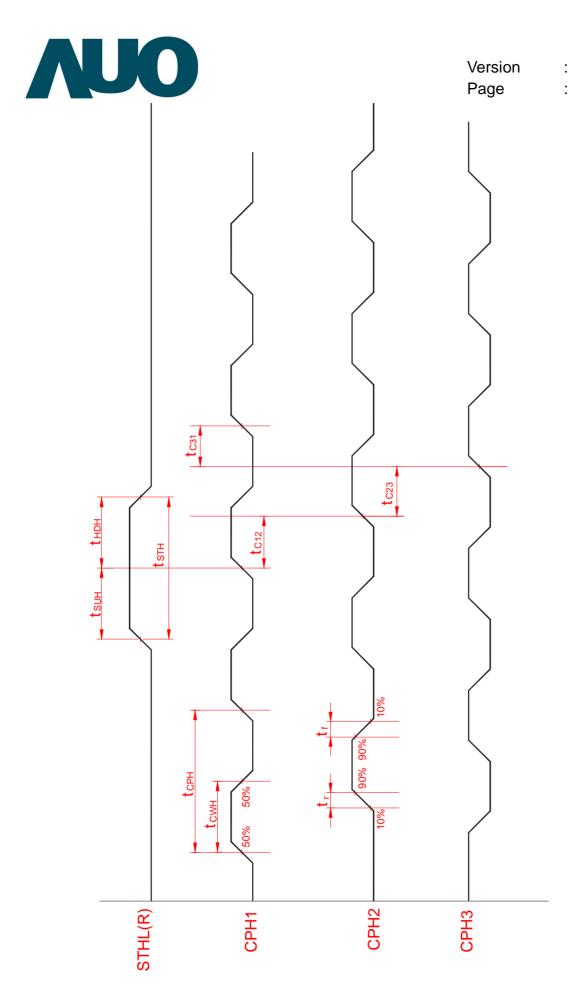


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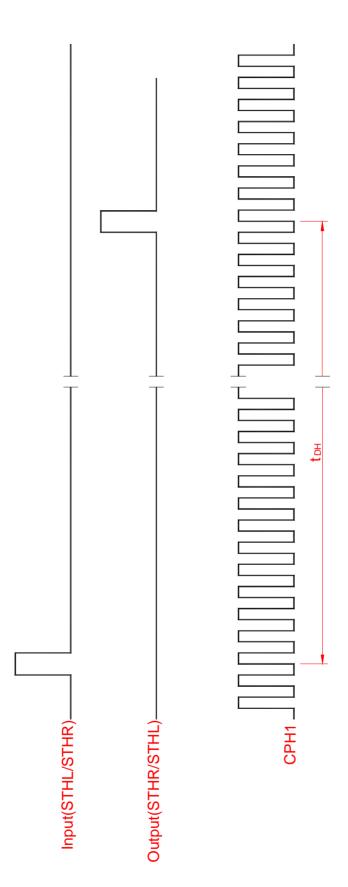
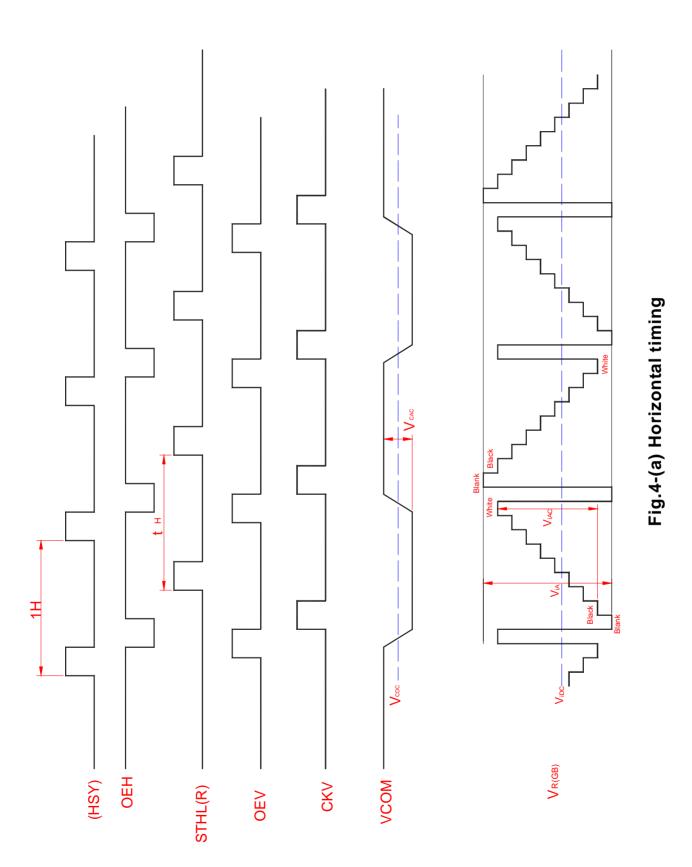


Fig.3 Horizontal display timing range



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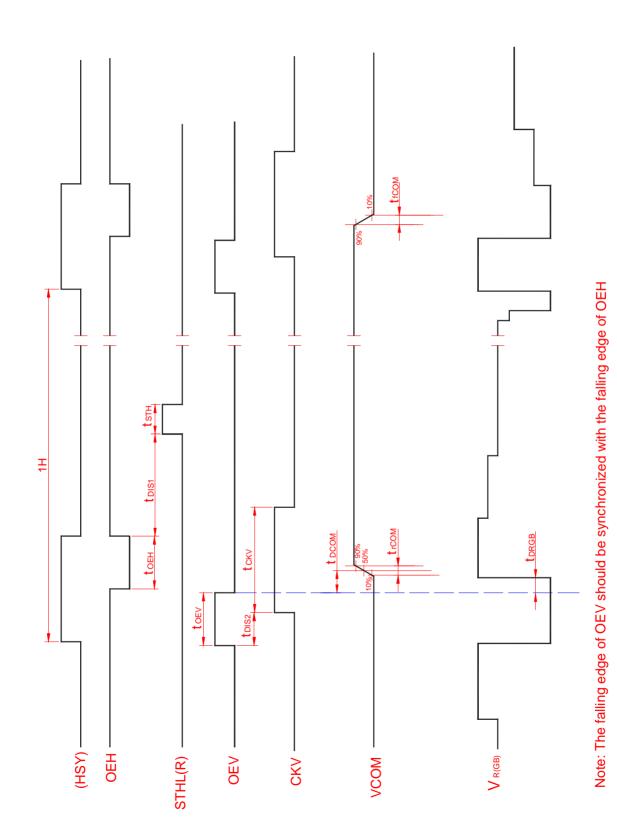
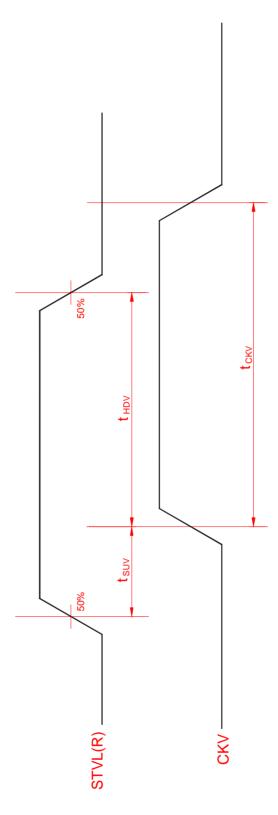


Fig.4-(b) Detail horizontal timing



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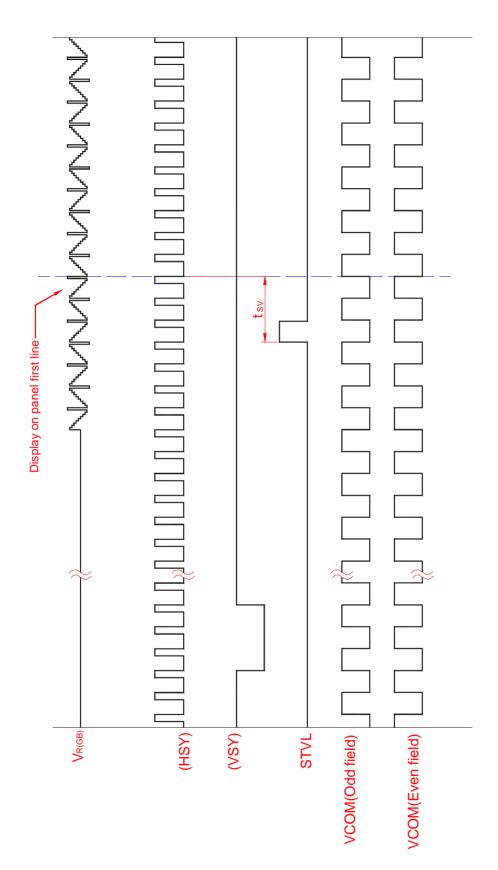


Fig.6-(a) Vertical timing (From up to down)



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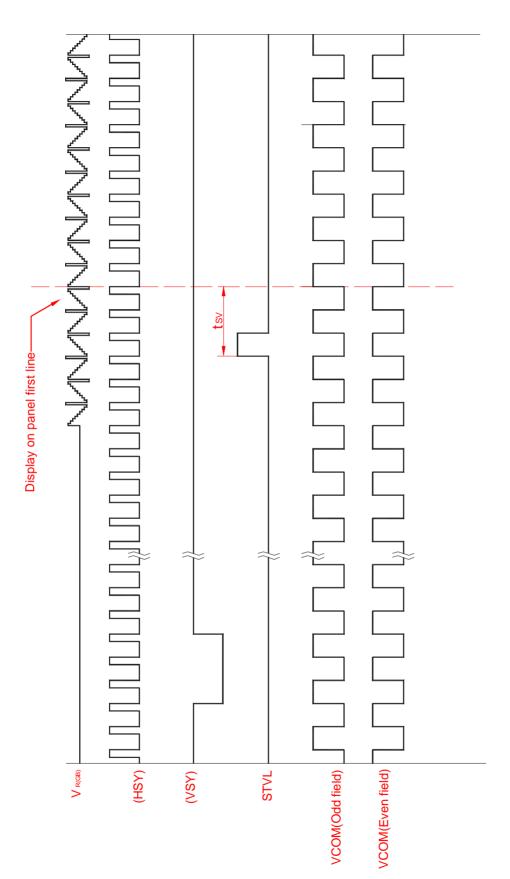


Fig.6-(b) Horizontal timing (From down to up)